

METHOD FOR CONTROLLING STRUCTURE OF CRYSTALLINE SURFACE

METHOD FOR CONTROLLING STRUCTURE OF CRYSTALLINE SURFACE

Patent Number: JP4325500
Publication date: 1992-11-13
Inventor(s): TERADA SATOSHI; others: 02
Applicant(s): SANYO ELECTRIC CO LTD
Requested Patent: ☐ JP4325500
Application Number: JP19910125560 19910426
Priority Number(s):
IPC Classification: C30B33/00; H01L21/02; H01L21/20
EC Classification:
EC Classification:
Equivalents: JP3043103B2

Abstract

PURPOSE: To realize a semiconductor device controlled at an atomic level by manufacturing a crystalline substrate having a sufficiently wide terrace part required for device operation.
CONSTITUTION: A crystalline substrate 1((a)) after surface polishing in which a terrace part 11 and a step part 12 are ununiformly distributed is subjected to growth by atomic layer epitaxy and is irradiated with light having spatial periodicity. The pattern of the terrace part 11 and the step part 12 is uniformized, and the step part 12 is multiplexed to spread the terrace part 11 ((b)).